



Docket No.: 1293.1128

#7/a
KW-S
#2-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Jae-seong Shim

Serial No. 09/612,971

Group Art Unit: 2133

Confirmation No. 9406

Filed: September 27, 2002

Examiner: Christine Tu

For: ERROR CORRECTION METHOD FOR HIGH DENSITY DISC

RECEIVED

AMENDMENT

DEC 30 2002

Assistant Commissioner for Patents
Washington, D.C. 20231

Technology Center 2100

Sir:

This is in response to the Office Action mailed September 27, 2002, and having a period for response set to expire on December 27, 2002.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

IN THE CLAIMS:

Please **REPLACE** claims 1, 2, 10, 15, 17, 29 and 30 as follows:

- a1
- Sub B1
1. (ONCE AMENDED) An error correction method adding an inner parity of e bytes and an outer parity of f bytes to an error correction block having a size of n bytes in a row direction x m bytes in a column direction, the error correction method comprising: